

Patent No.	Document ID	Issue Date	Pages	Title	Current GR	Current XRef	Retrieval XRef	Inventor	S	C	P	W	U	Image
US 2005/012591 A1	20050016	18	System architecture for reducing system usage and improving linearity in an amplifier system	350/297			Robinson, Ian	P	C	P	U	W	U	US 2005/012591 A1
US 2005/0110548 A1	20050026	19	Modulated signal amplifier architecture	350/151	350/199		Robinson, Ian et al.	P	C	P	U	W	U	US 2005/0110548 A1
US 2005/0110995 A1	20050026	16	Multiple polar amplifier architecture	350/124R			Robinson, Ian	P	C	P	U	W	U	US 2005/0110995 A1
US 2005/0110962 A1	20050026	14	Variable supply amplifier system	350/125			Robinson, Ian et al.	P	C	P	U	W	U	US 2005/0110962 A1
US 2005/0098826 A1	20050031	14	Switching amplifier architecture	350/103			Robinson, Ian et al.	P	C	P	U	W	U	US 2005/0098826 A1
US 2005/0017832 A1	20050127	16	Digital cross cancellation system	350/151	350/199		Robinson, Ian	P	C	P	U	W	U	US 2005/0017832 A1
US 2005/0016900 A1	20050127	19	System architecture for reducing system usage and improving linearity in an amplifier system	350/149			Robinson, Ian	P	C	P	U	W	U	US 2005/0016900 A1
US 2004/0260156 A1	20041130	19	Multi-mode amplifier system	450/91	450/127.1		Robinson, Ian et al.	P	C	P	U	W	U	US 2004/0260156 A1
US 2004/0260346 A1	20041130	20	MULTIMODE MULTITASKER POWER AMPLIFIER	350/124R			Robinson, Ian et al.	P	C	P	U	W	U	US 2004/0260346 A1
US 2004/0252475 A1	20041230	17	Power and linear amplifier system	350/103			Webster, Robert et al.	P	C	P	U	W	U	US 2004/0252475 A1